



# TEST REPORT: HVG-240-54

## 240W Constant Voltage + Constant Current LED Driver

### ■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

Protection Function Test

Control Function Test

Component Stress Test

### ■ SAFETY & E.M.C. TEST

Safety Test

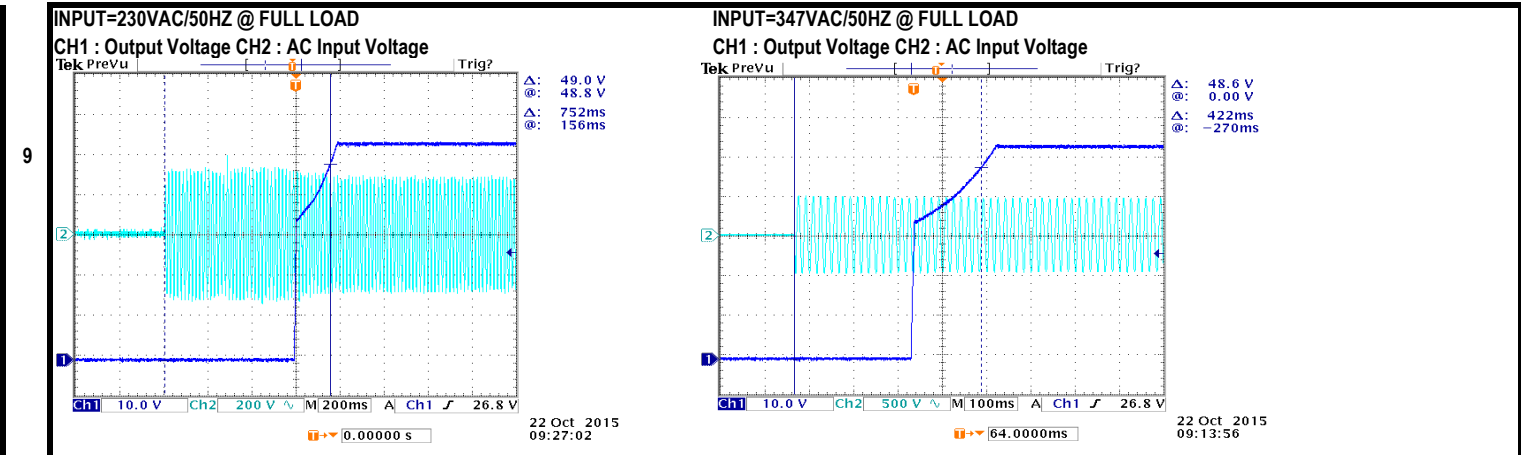
E.M.C. Test

### ■ RELIABILITY TEST

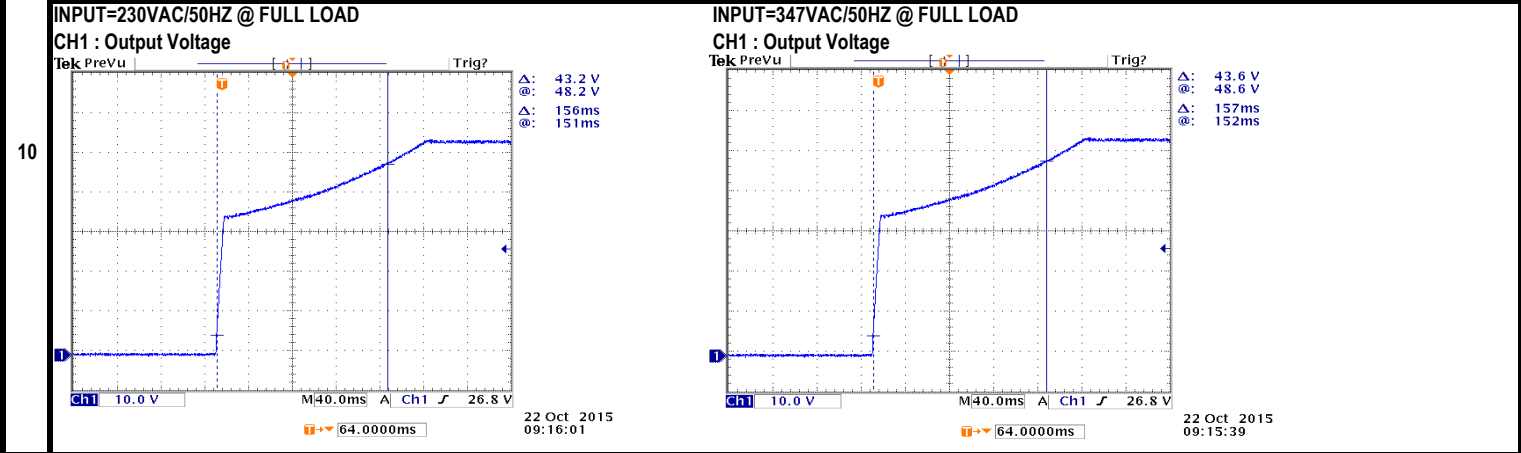
ENVIRONMENT TEST

DESIGN VERIFY TEST  
OUTPUT FUNCTION TEST

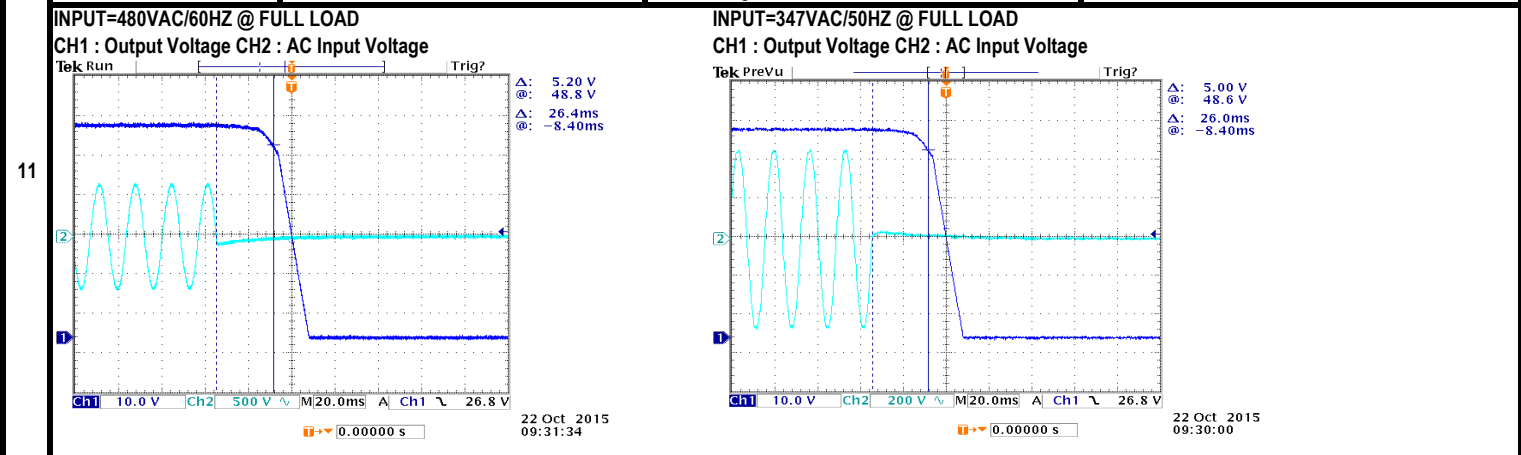
NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OUTPUT VOLTAGE ADJUST RANGE	CH1: 50.00V ~ 57.00V	I/P : 347VAC O/P: MIN LOAD TA : 25°C	CH1: 47.88V ~ 58.96V
2	CONSTANT CURRENT REGION	CH1: 27V ~ 54V	I/P : 347VAC O/P: FULL LOAD TA : 25°C	CH1: 0.55V ~ 53V
3	CURRENT ADJ. RANGE	CH1: 2.25A ~ 4.5A	I/P : 347VAC I/P : 230VAC O/P: CV MIN& CV MAX-1V TA : 25°C	1.68A ~ 4.74A 347VAC-CV MAX-1V 1.59A 4.74A 347VAC-CV MIN 1.70A ~ 4.732A 230VAC-CV MAX-1V 1.60A 4.74A 230VAC-CV MIN
4	OUTPUT VOLTAGE TOLERANCE (Max)	V1 : 1.0% ~ -1.0%	I/P : 180VAC / 528VAC O/P: FULL / MINLOAD TA= 25°C	V1: -0.09% ~ 0.04%
5	LINE REGULATION (MAX.)	V1 : 0.5% ~ -0.5%	I/P : 180VAC / 528VAC O/P: FULL LOAD TA : 25°C	V1: 0.00% ~ 0.04%
6	LOAD REGULATION (MAX.)	V1 : 0.5% ~ -0.5%	I/P : 347VAC O/P: MIN LOAD ~ FULL LOAD TA : 25°C	V1: -0.05% ~ 0.04%
7	OVER/UNDERSHOOT TEST	< ±5%	I/P : 347VAC O/P: FULL LOAD TA : 25°C	TEST < ±5%
	RIPPLE & NOISE(Max)	V1 : 350 mVp-p	I/P : 347VAC O/P: FULL LOAD TA : 25°C	V1 : 178 mVp-p
8	<p>low frequency : Tek PreVu Trig? Δ: 89.0mV @: 78.0mV Δ: 5.60ms @: -7.12ms Ch1 Pk-Pk 178mV 50.0mV/div 4.00ms/div 22 Oct 2015 08:54:29</p>			
	SET UP TIME (MAX.)	347VAC : 500ms 230VAC : 500ms 480VAC : 500ms	I/P : 347VAC I/P : 230VAC I/P : 480VAC O/P: FULL LOAD TA : 25°C	347VAC: 302ms 230VAC 321ms 480VAC 302ms



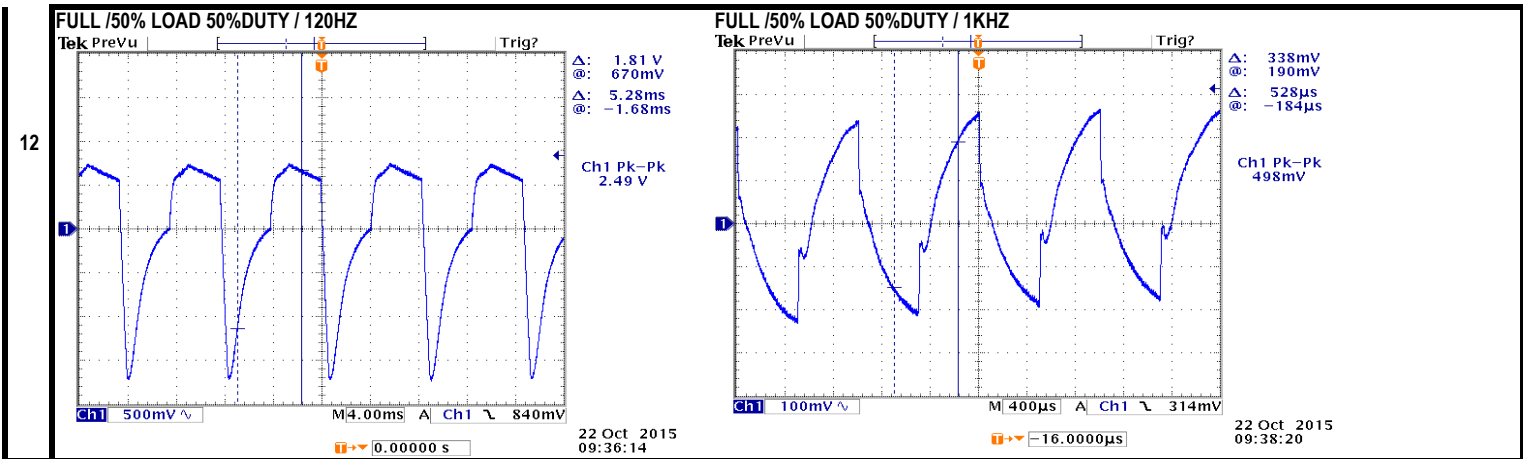
<b>RISE TIME (MAX.)</b>	347VAC : 150ms	I/P : 347VAC	347VAC: 58.6ms
	230VAC : 150ms	I/P : 230VAC	230VAC : 57.2ms
	480VAC : 150ms	I/P : 480VAC	480VAC : 57.2ms
	O/P: FULL LOAD		
		TA : 25°C	



<b>HOLD UP TIME (TYP.)</b>	347VAC : 12ms	I/P : 347VAC	347VAC: 26.0ms
	480VAC : 12ms	I/P : 480VAC	480VAC: 26.4ms
		O/P: FULL LOAD	
		TA : 25°C	

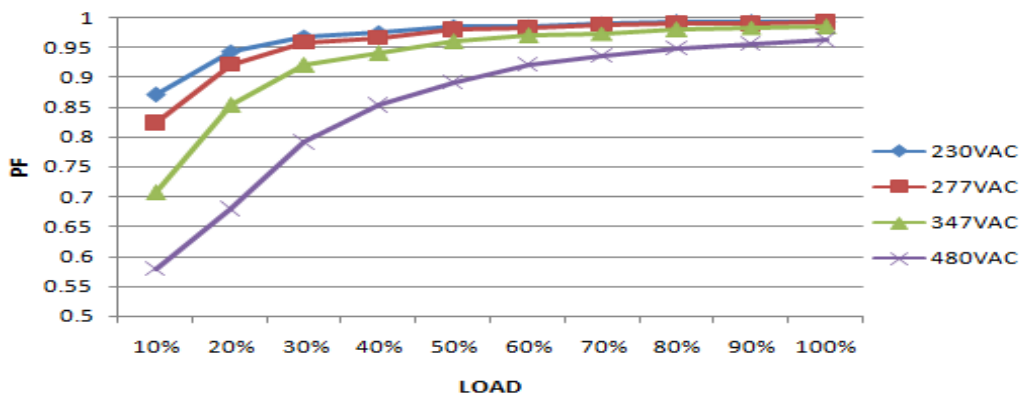


<b>DYNAMIC LOAD</b>	V1 : 5400 mVp-p	I/P : 347VAC	(1). (2). unit:mVp-p
		O/P:	V1: 2490mv 498mv
		(1)Full/Min load 50%duty/120HZ	
		(2)Full/Min load 50%duty/1KHZ	
		TA : 25°C	

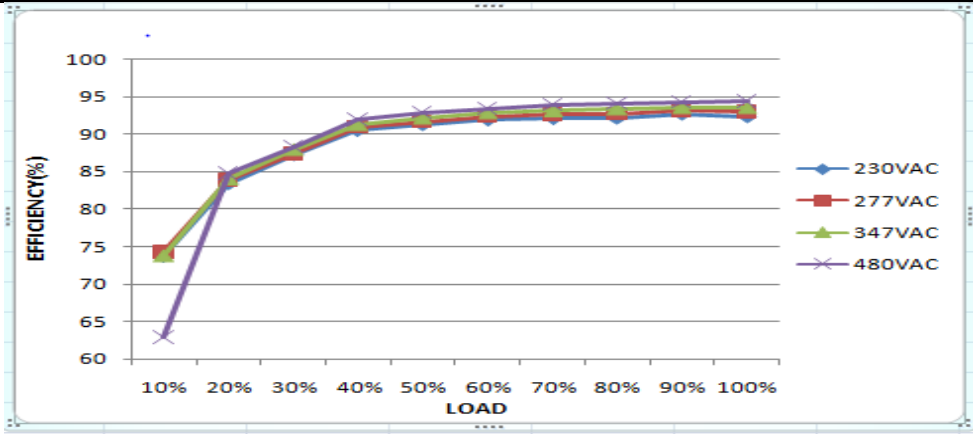


### INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	180VAC ~ 528VAC	I/P : TESTING O/P : FULL LOAD Ta : 25°C	138.1VAC ~ 528VAC
			I/P : LOW-LINE = 177VAC HIGH-LINE = 538VAC O/P : FULL/MIN LOAD ON:30 Sec ; OFF:30 Sec 10MIN ( POWER ON/OFF NO DAMAGE )	TEST : OK
2	INPUT FREQUENCY RANGE	47HZ ~ 63HZ NO DAMAGE	I/P : 180VAC ~ 528VAC O/P : FULL-MIN LOAD Ta : 25°C	TEST : OK
3	INPUT CURRENT (TYP.)	0.8 / 347VAC 0.6 / 480VAC	I/P : 347VAC I/P : 480VAC O/P : FULL LOAD TA : 25°C	I= 0.787 / 347VAC I= 0.5614 / 480VAC
4	LEAKAGE CURRENT	< 0.75mA	I/P : 480VAC O/P : MIN LOAD TA : 25°C	L-FG: 0.32 mA N-FG: 0.34 mA
5	POWER FACTOR (TYP.)	0.95 / 347VAC	I/P : 347VAC	PF= 0.9885 / 347VAC
		0.93 / 480VAC	I/P : 480VAC	PF= 0.9652 / 480VAC
		0.97 / 277VAC	I/P : 277VAC	PF= 0.9915 / 277VAC
		0.98 / 230VAC	I/P : 230VAC O/P : FULL LOAD TA : 25°C	PF= 0.9942 / 230VAC

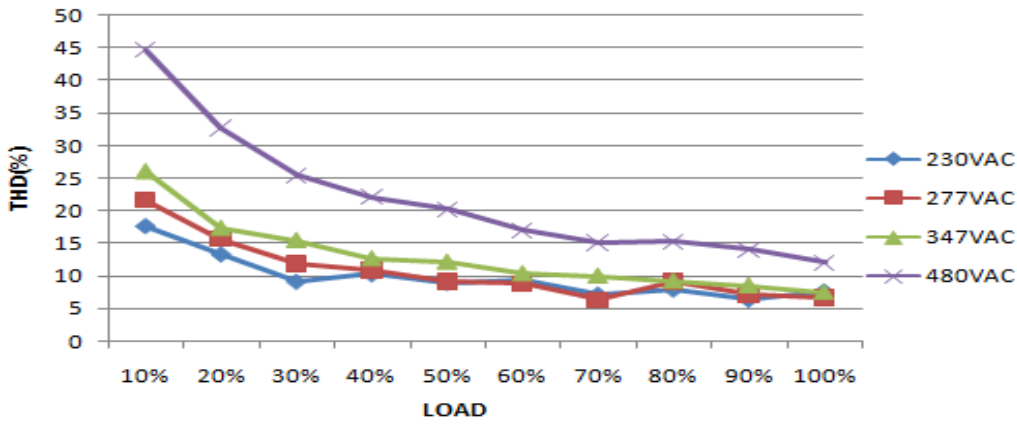


EFFICIENCY (TYP.)	93.0%	I/P : 347VAC O/P : FULL LOAD TA : 25°C	93.17 %
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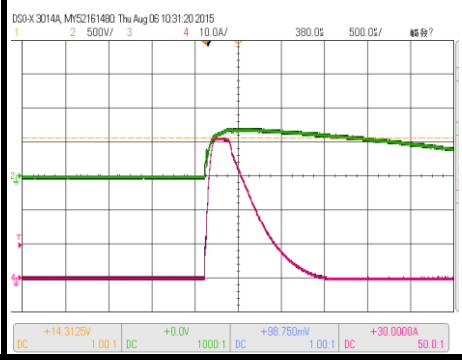
TOTAL HARMONIC DISTORTION	Total harmonic distortion will be lower than 20% when output loading is 50% or higher at 230VAC / 277VAC / 347VAC / 480VAC	I/P : 347VAC / 50% LOAD I/P : 480VAC / 60% LOAD TA : 25°C	THD : 11.867 / 347VAC THD : 13 / 480VAC
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INRUSH CURRENT (TYP.)	50A / 480VAC	I/P : 480VAC	I= 41.3 / 480VAC
	width= 532 us measured at 50% Ipeak COLD START	O/P: FULL LOAD TA : 25°C	T50= 540 us

INPUT=480VAC/50HZ @ FULL LOAD  
CH2 : Input current (1V=1A) CH4 : AC Input Voltage



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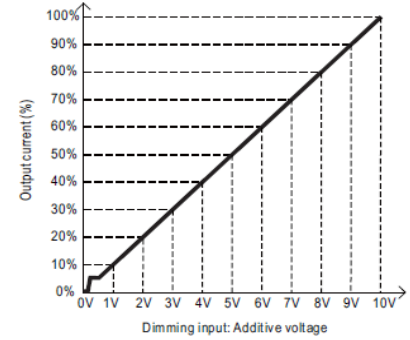
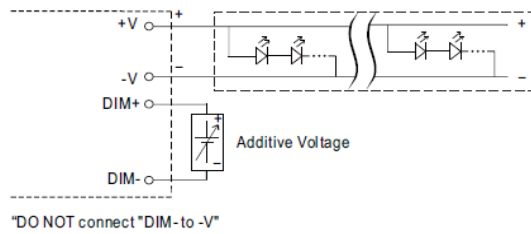
## DIMMING OPERATION (for B-Type)

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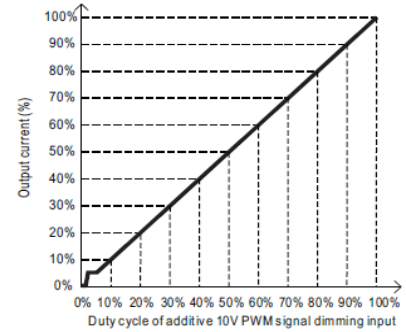
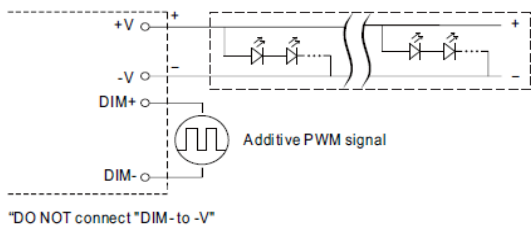
### ※ 3 in 1 dimming function (for B-Type)

- Output constant current level can be adjusted by applying one of the three methodologies between DIM+ and DIM-: 0 ~ 10VDC, or 10V PWM signal or resistance.
- Direct connecting to LEDs is suggested. It is not suitable to be used with additional drivers.
- Dimming source current from power supply: 100 $\mu$ A (typ.)

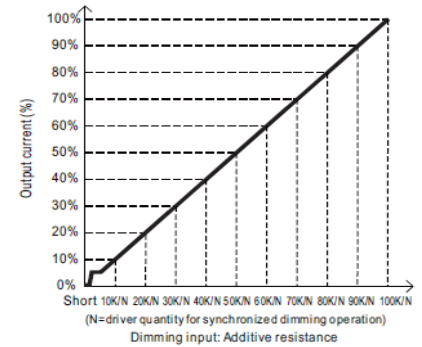
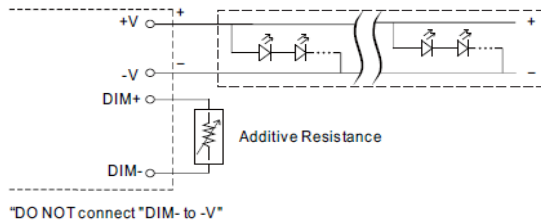
#### ⊙ Applying additive 0 ~ 10VDC



#### ⊙ Applying additive 10V PWM signal (frequency range 100Hz ~ 3KHz):



#### ⊙ Applying additive resistance:



- Note : 1. Min. dimming level is about 5% and the output current is not defined when 0% < I<sub>out</sub> < 5%.  
 2. The output current could drop down to 0% when dimming input is about 0k  $\Omega$  or 0Vdc, or 10V PWM signal with 0% duty cycle.

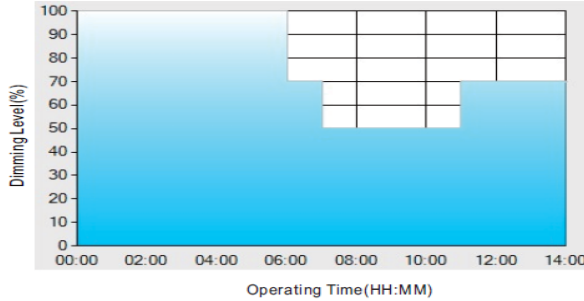
	R	SHORT	10K	20K	30K	40K	50K	60K	70K	80K	90K	100K	OPEN
	54V	O/P CURRENT	0.00000A	0.460A	0.903A	1.352A	1.821A	2.239A	2.649A	3.071A	3.472A	3.883A	4.267A
	%	0.00%	10.22%	20.07%	30.04%	40.47%	49.76%	58.87%	68.24%	77.16%	86.29%	94.82%	103.62%
	V	0V	1V	2V	3V	4V	5V	6V	7V	8V	9V	10V	OPEN
	O/P CURRENT	0.00000A	0.464A	0.928A	1.397A	1.899A	2.321A	2.741A	3.206A	3.642A	4.073A	4.519A	4.663A
	%	0.00%	10.31%	20.62%	31.04%	42.20%	51.58%	60.91%	71.24%	80.93%	90.51%	100.42%	103.62%
	PWM (100HZ)	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%	OPEN
	O/P CURRENT	0.00000A	0.486A	0.933A	1.395A	1.886A	2.305A	2.742A	3.175A	3.612A	4.048A	4.483A	4.663A
	%	0.00%	10.80%	20.73%	31.00%	41.91%	51.22%	60.93%	70.56%	80.27%	89.96%	99.62%	103.62%

**DIMMING OPERATION**  
(for Dxx-Type by User definition)

※ Smart timer dimming function (for Dxx-Type by User definition)

MEAN WELL Smart timer dimming primarily provides the adaptive proportion dimming profile for the output constant current level to perform up to 14 consecutive hours. 3 dimming profiles hereunder are defined accounting for the most frequently seen applications. If other options may be needed, please contact MEAN WELL for details.

Ex : ☉ D01-Type: the profile recommended for residential lighting



Set up for D01-Type in Smart timer dimming software program:

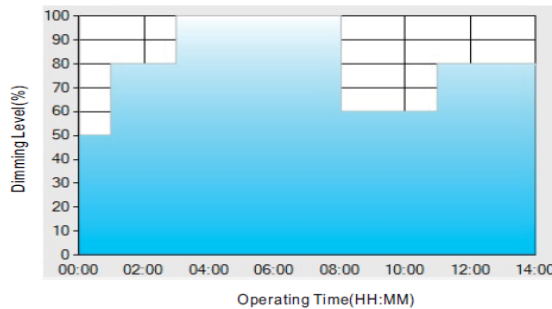
	T1	T2	T3	T4
TIME**	06:00	07:00	11:00	---
LEVEL**	100%	70%	50%	70%

\*\* : TIME matches Operating Time in the diagram whereas LEVEL matches Dimming Level.

Example: If a residential lighting application adopts D01-Type, when turning on the power supply at 6:00pm, for instance:

- [1] The power supply will switch to the constant current level at 100% starting from 6:00pm.
  - [2] The power supply will switch to the constant current level at 70% in turn, starting from 0:00am, which is 06:00 after the power supply turns on.
  - [3] The power supply will switch to the constant current level at 50% in turn, starting from 1:00am, which is 07:00 after the power supply turns on.
  - [4] The power supply will switch to the constant current level at 70% in turn, starting from 5:00am, which is 11:00 after the power supply turns on.
- The constant current level remains till 8:00am, which is 14:00 after the power supply turns on.

Ex : ☉ D02-Type: the profile recommended for street lighting



Set up for D02-Type in Smart timer dimming software program:

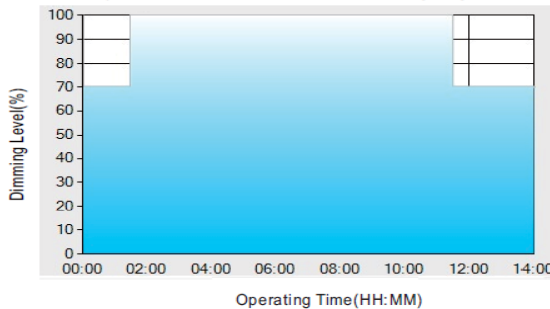
	T1	T2	T3	T4	T5
TIME**	01:00	03:00	8:00	11:00	---
LEVEL**	50%	80%	100%	60%	80%

\*\* : TIME matches Operating Time in the diagram whereas LEVEL matches Dimming Level.

Example: If a street lighting application adopts D02-Type, when turning on the power supply at 5:00pm, for instance:

- [1] The power supply will switch to the constant current level at 50% starting from 5:00pm.
  - [2] The power supply will switch to the constant current level at 80% in turn, starting from 6:00pm, which is 01:00 after the power supply turns on.
  - [3] The power supply will switch to the constant current level at 100% in turn, starting from 8:00pm, which is 03:00 after the power supply turns on.
  - [4] The power supply will switch to the constant current level at 60% in turn, starting from 1:00am, which is 08:00 after the power supply turns on.
  - [5] The power supply will switch to the constant current level at 80% in turn, starting from 4:00am, which is 11:00 after the power supply turns on.
- The constant current level remains till 6:30am, which is 14:00 after the power supply turns on.

Ex : ☉ D03-Type: the profile recommended for tunnel lighting



Set up for D03-Type in Smart timer dimming software program:

	T1	T2	T3
TIME**	01:30	11:00	---
LEVEL**	70%	100%	70%

\*\* : TIME matches Operating Time in the diagram whereas LEVEL matches Dimming Level.

Example: If a tunnel lighting application adopts D03-Type, when turning on the power supply at 4:30pm, for instance:

- [1] The power supply will switch to the constant current level at 70% starting from 4:30pm.
  - [2] The power supply will switch to the constant current level at 100% in turn, starting from 6:00pm, which is 01:30 after the power supply turns on.
  - [3] The power supply will switch to the constant current level at 70% in turn, starting from 5:00am, which is 11:00 after the power supply turns on.
- The constant current level remains till 6:30am, which is 14:00 after the power supply turns on.

## PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OVER LOAD PROTECTION	95% ~ 108%	I/P: 528VAC I/P: 180VAC O/P: TESTING TA: 25°C	101% /528VAC 100% /180 VAC Constant Current Limiting
2	OVER VOLTAGE PROTECTION	60.00V ~ 67.00V	I/P: 528VAC I/P: 180VAC O/P: MIN LOAD TA: 25°C	I/P:528\ 63.43 V I/P:180\ 63.37 V Shut down Re- power ON
3	OVER TEMPERATURE PROTECTION	Shut down Re- power ON	I/P: 347VAC O/P: FULL LOAD	O.T.P. Active Shut down Re- power ON
4	SHORT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P: 528VAC O/P: FULL LOAD Ta: 25°C	NO DAMAGE Constant Current Limiting

## COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM Power Transistor	Q901 Rated : 950V 9.0A	I/P : 531VAC  VDS : O/P : (1)Full Load Turn on (2) Output Short (3)Full load continue (4)Dynamic Load Full/Min Load 90%Duty/1KHz (5)Dynamic Load Full/Min Load 90%Duty/5KHz (6)Dynamic Load Full/Min Load 50%Duty/120Hz (7)0%→400% Load Ta : 25°C	VIN: 531VAC VDS: (1). 820.0V (2). 824.0V (3). 800.0V (4). 816.0V (5). 836.0V (6). 832.0V (7). 828.0V
2	O/P Diode (MOSFET)	Q101 Rated : 150V 117.0A Q102 Rated : 150V 117.0A	I/P : 531VAC  VDS : O/P : (1)Full Load Turn on (2) Output Short (3)Full load continue (4)Dynamic Load Full/Min Load 90%Duty/1KHz (5)Dynamic Load Full/Min Load 90%Duty/5KHz (6)Dynamic Load Full/Min Load 50%Duty/120Hz (7)0%→400% Load (8) NO LOAD Ta : 25°C	Q101 Q102 VDS : VDS : (1). 118.0V 118.0V (2). 17.9V 16.2V (3). 118.0V 118.0V (4). 118.0V 119.0V (5). 118.0V 118.0V (6). 118.0V 120.0V (7). 17.7V 15.1V (8). 114.0V 119.0V
3	Input Capacitor	C5 Rated : 82uf 450V	I/P : 531VAC O/P : (1)Full Load Turn on /Off (2)Min load Turn on /Off (3)Full Load /Min load Change Ta : 25°C	(1). 380.0V (2). 412.0V (3). 394.0V
4	Control IC	U1 Rated : 20V (max) 10V (min)	I/P : 531VAC O/P : (1)Full Load (2)Output Short Change (4)O.V.P (5)Low Line No Load Vo(min) Ta : 25°C	U1 (1). 13.3V (2). 13.3V (3). 13.2V (4). 13.3V (5). 13.3V
5	PFC Power Transistor	Q1 Rated : 1050V 9.0A	I/P : 531VAC  VDS : O/P : (1)Full Load Turn on (2) Output Short (3)Full load continue PASS (4)Dynamic Load Full/Min Load 90%Duty/1KHz (5)Dynamic Load Full/Min Load 90%Duty/5KHz (6)Dynamic Load Full/Min Load 50%Duty/120Hz (7)0%→400% Load	VIN: 531VAC VDS: (1). 836.0V (2). 836.0V (3). 844.0V (4). 844.0V (5). 832.0V (6). 824.0V (7). 836.0V



Ta : 25°C

**SAFETY & E.M.C. TEST**  
**SAFETY TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	I/P-O/P : 3.75KVAC /min I/P-FG : 2.0KVAC /min O/P-FG : 1.5KVAC /min	I/P-O/P: 4.13KVAC /min I/P-FG: 2.40KVAC /min O/P-FG: 1.80KVAC /min Ta : 25°C	I/P-O/P: 1.99mA I/P-FG: 1.883mA O/P-FG: 1.035mA  NO DAMAGE
2	ISOLATION RESISTANCE	I/P-O/P : 500VDC>100MΩ I/P-FG : 500VDC>100MΩ O/P-FG : 500VDC>100MΩ	I/P-O/P: 500VDC I/P-FG: 500VDC O/P-FG: 500VDC Ta : 25°C/70%RH	I/P-O/P: 6.1G Ω I/P-FG: 3.93G Ω O/P-FG: 20.3G Ω  NO DAMAGE
3	GROUNDING CONTINUITY	FG(PE) TO CHASSIS OR TRACE < 100 mΩ	40 A / 2min Ta : 25°C/70%RH	26 mΩ

**E.M.C. TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	CONDUCTION	FCC Part 15 Subpart B	I/P : 440VAC/60HZ O/P : FULL LOAD / 30% LOAD Ta : 25°C	PASS Test by certified Lab
2	RADIATION	FCC Part 15 Subpart B	I/P : 480VAC/60HZ O/P : FULL LOAD Ta : 25°C	PASS Test by certified Lab
3	E.S.D	EN61000-4-2 LIGHT INDUSTRY AIR : 8KV / Contact : 4KV	I/P : 230VAC/50HZ O/P : FULL LOAD Ta : 25°C	CRITERIA A
4	E.F.T	EN61000-4-4 LIGHT INDUSTRY INPUT : 1KV	I/P : 230VAC/50HZ O/P : FULL LOAD Ta : 25°C	CRITERIA A
5	SURGE	IEC61000-4-5 INDUSTRY L-N : 2KV;L/N-PE : 4KV	I/P : 230VAC/50HZ O/P : FULL LOAD Ta : 25°C	CRITERIA A
6	Test by certified Lab & Test Report Prepare. Any contradictions of the test results, please refer to the latest EMC test report.			

**RELIABILITY TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT																																																																												
1	TEMPERATURE RISE TEST	MODEL : HVG-240-48 1. ROOM AMBIENT BURN-IN : 3.0hrs IP: 230VAC O/P: 100% LOAD TA= 24.8°C 2. HIGH AMBIENT BURN-IN : 2.0hrs IP: 230VAC O/P: 100% LOAD TA= 59.2°C	<table border="1"> <thead> <tr> <th>CH.</th> <th>Position</th> <th>ROOM AMBIENT Ta= 24.8 °C</th> <th>HIGH AMBIENT Ta= 59.2 °C</th> </tr> </thead> <tbody> <tr><td>1</td><td>BD1</td><td>62.7°C</td><td>94.9°C</td></tr> <tr><td>2</td><td>Q1</td><td>63.0°C</td><td>96.3°C</td></tr> <tr><td>3</td><td>Q901</td><td>64.3°C</td><td>98.3°C</td></tr> <tr><td>4</td><td>L2</td><td>61.7°C</td><td>93.6°C</td></tr> <tr><td>5</td><td>C2</td><td>60.2°C</td><td>90.5°C</td></tr> <tr><td>6</td><td>C10</td><td>63.0°C</td><td>95.1°C</td></tr> <tr><td>7</td><td>L1</td><td>66.5°C</td><td>100.0°C</td></tr> <tr><td>8</td><td>ZNR2</td><td>73.2°C</td><td>104.2°C</td></tr> <tr><td>9</td><td>RTH3</td><td>60.5°C</td><td>94.3°C</td></tr> <tr><td>10</td><td>T1</td><td>62.7°C</td><td>98.0°C</td></tr> <tr><td>11</td><td>C46</td><td>62.9°C</td><td>96.7°C</td></tr> <tr><td>12</td><td>C54</td><td>61.1°C</td><td>94.8°C</td></tr> <tr><td>13</td><td>Q102</td><td>59.2°C</td><td>93.5°C</td></tr> <tr><td>14</td><td>C102</td><td>57.8°C</td><td>91.6°C</td></tr> <tr><td>15</td><td>C201</td><td>61.2°C</td><td>94.7°C</td></tr> <tr><td>16</td><td>C200</td><td>59.7°C</td><td>93.4°C</td></tr> <tr><td>17</td><td>U1</td><td>64.1°C</td><td>96.0°C</td></tr> <tr><td>18</td><td>C5</td><td>63.1°C</td><td>96.6°C</td></tr> </tbody> </table>	CH.	Position	ROOM AMBIENT Ta= 24.8 °C	HIGH AMBIENT Ta= 59.2 °C	1	BD1	62.7°C	94.9°C	2	Q1	63.0°C	96.3°C	3	Q901	64.3°C	98.3°C	4	L2	61.7°C	93.6°C	5	C2	60.2°C	90.5°C	6	C10	63.0°C	95.1°C	7	L1	66.5°C	100.0°C	8	ZNR2	73.2°C	104.2°C	9	RTH3	60.5°C	94.3°C	10	T1	62.7°C	98.0°C	11	C46	62.9°C	96.7°C	12	C54	61.1°C	94.8°C	13	Q102	59.2°C	93.5°C	14	C102	57.8°C	91.6°C	15	C201	61.2°C	94.7°C	16	C200	59.7°C	93.4°C	17	U1	64.1°C	96.0°C	18	C5	63.1°C	96.6°C	
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2	LOW TEMPERATURE TURN ON TEST	NO DAMAGE 1 HOUR ( MIN )	I/P : 528VAC / 180VAC O/P : FULL LOAD Ta : -45.0°C	TEST : OK
3	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 60°C NO DAMAGE	I/P : 528VAC O/P : FULL LOAD Ta : 60°C HUMIDITY= 95.0% RH	TEST : OK
4	TEMPERATURE COEFFICIENT	±0.03% /°C(0~60°C)	I/P : 347VAC O/P : FULL LOAD	±0.01% /°C(0~60°C)
5	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature : -45°C ~ +90°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 5 CYCLE 5. Input/Output condition : STATIC		TEST : OK
6	THERMAL SHOCK TEST	1. Thermal shock Temperature : -45°C ~ 65°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 16 CYCLE 5. Input/Output condition : 230V Full Load AC ON/OFF t: turn on 3sec ; turn off 1sec @ 15cycle Full Load burn in@ 1cycle		TEST : OK
7	VIBRATION TEST	1 Carton & 1 Set (1) Waveform : Sine Wave (2) Frequency : 10~500Hz (4) Acceleration : 5G (5) Test Time : 72min in each axis (X.Y.Z) (6) Ta : 25°C		TEST : OK
8	CAPACITOR LIFE CYCLE	HVG-240-48 :SUPPOSE C102 IS THE MOST CRITICAL COMPONENT (1) I/P : 347VAC O/P : FULL LOAD Tc= 80°C LIFE TIME (2) I/P : 347VAC O/P : 75% LOAD Tc= 80°C LIFE TIME (3) I/P : 347VAC O/P : 50% LOAD Tc= 80°C LIFE TIME		(1). 52071 HRS (2). 58979 HRS (3). 67152 HRS
9	MTBF	Conducted by Parts Stress Analysis Prediction 114.5K hrs min. MIL-HDBK-217F (25°C)		
10	Ongoing Reliability Test	I/P : 230VAC O/P : FULL LOAD TA=50°C Demonstration Mean Time Between Failure : 50,000 hours		

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	DANIEL GAO	SANFORD SU	VINCENT ZENG